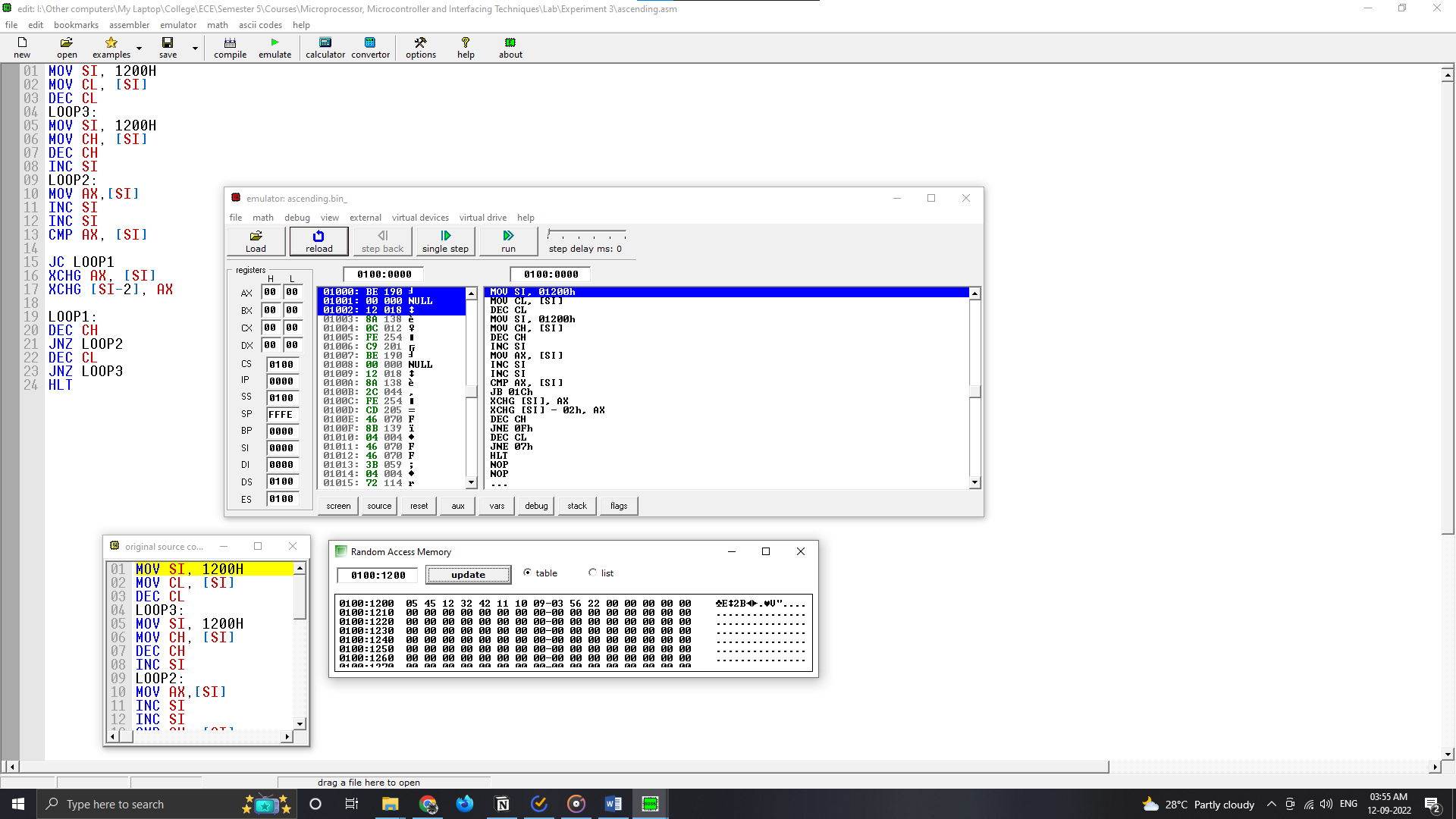
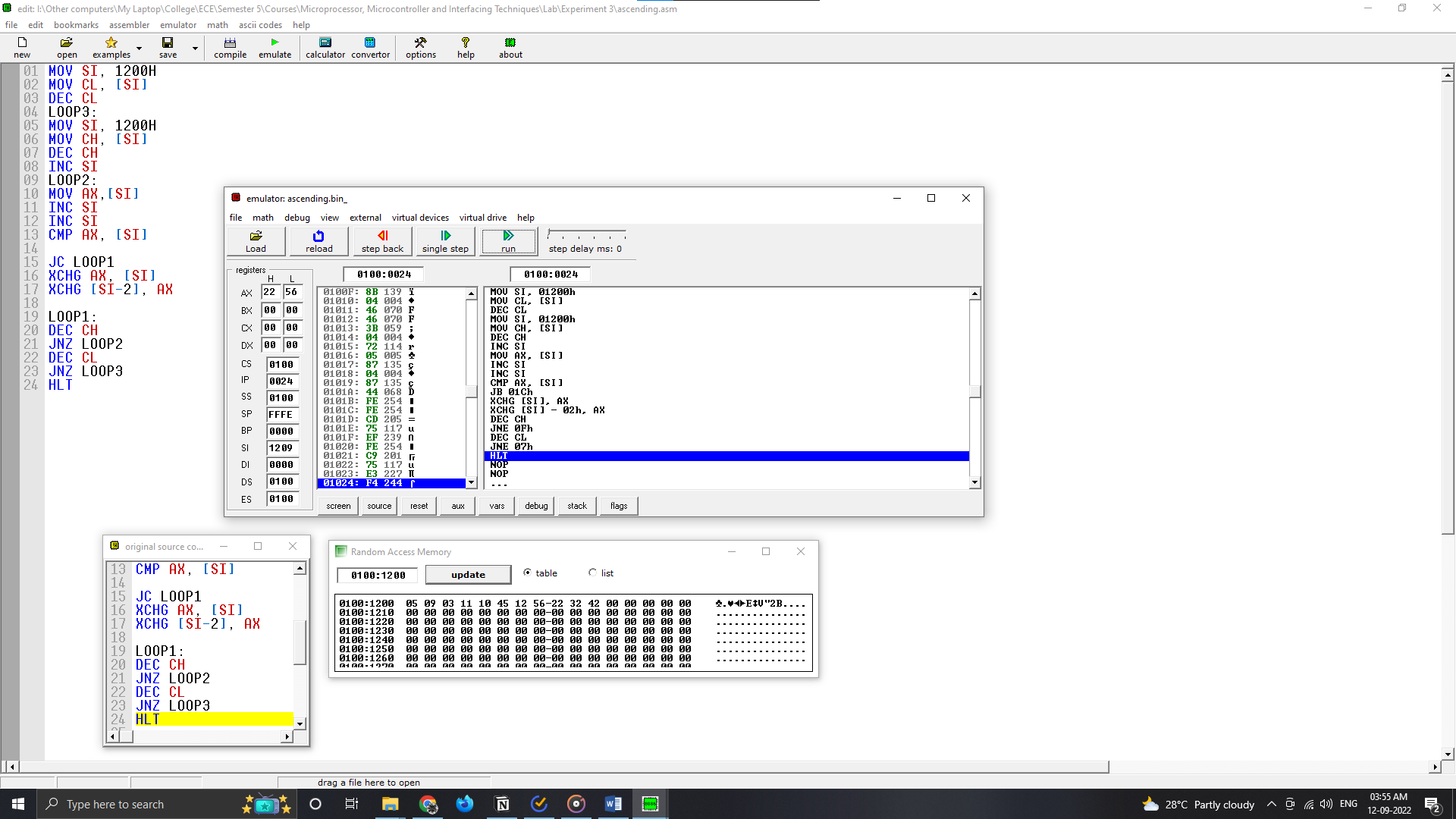
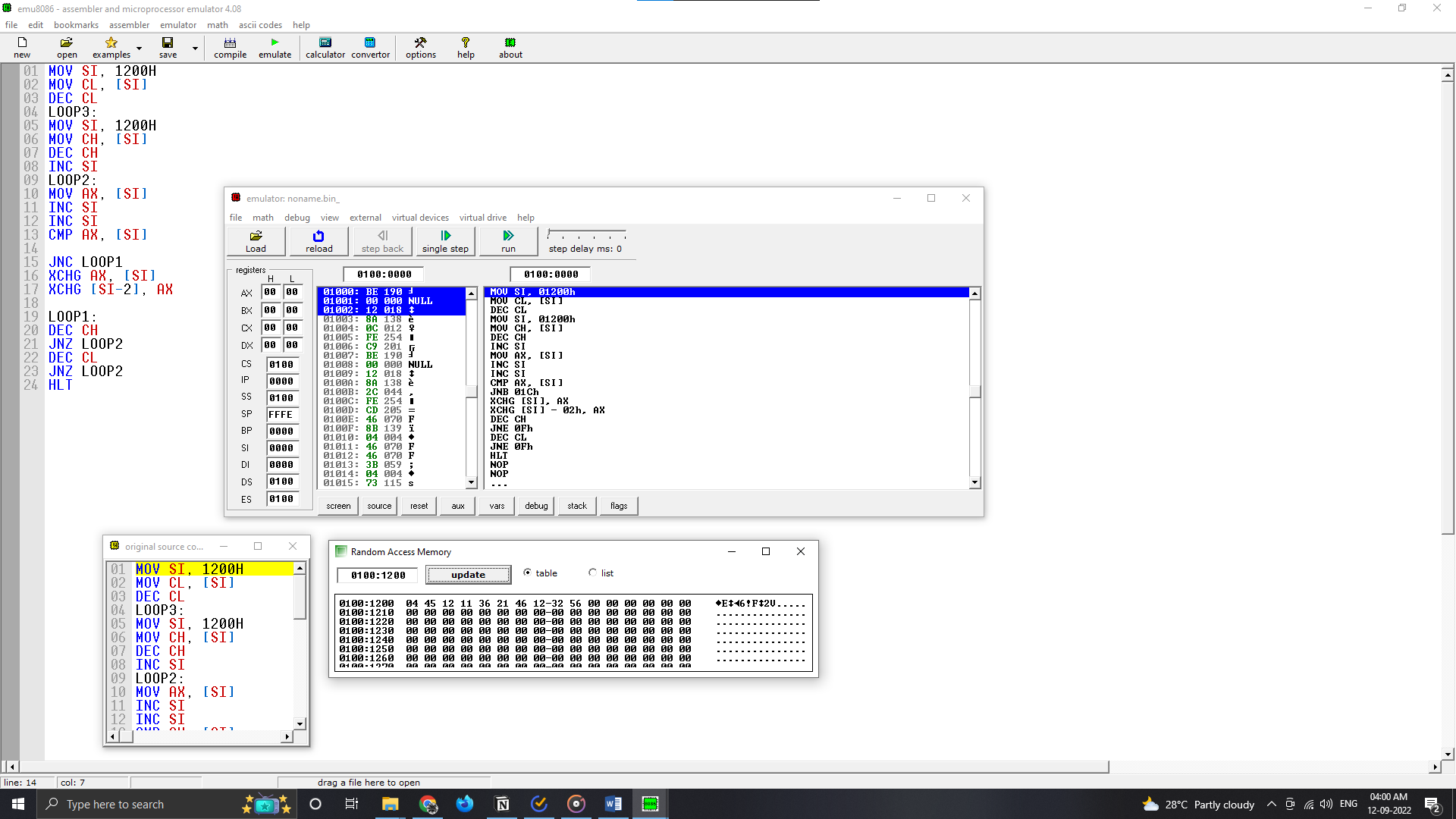
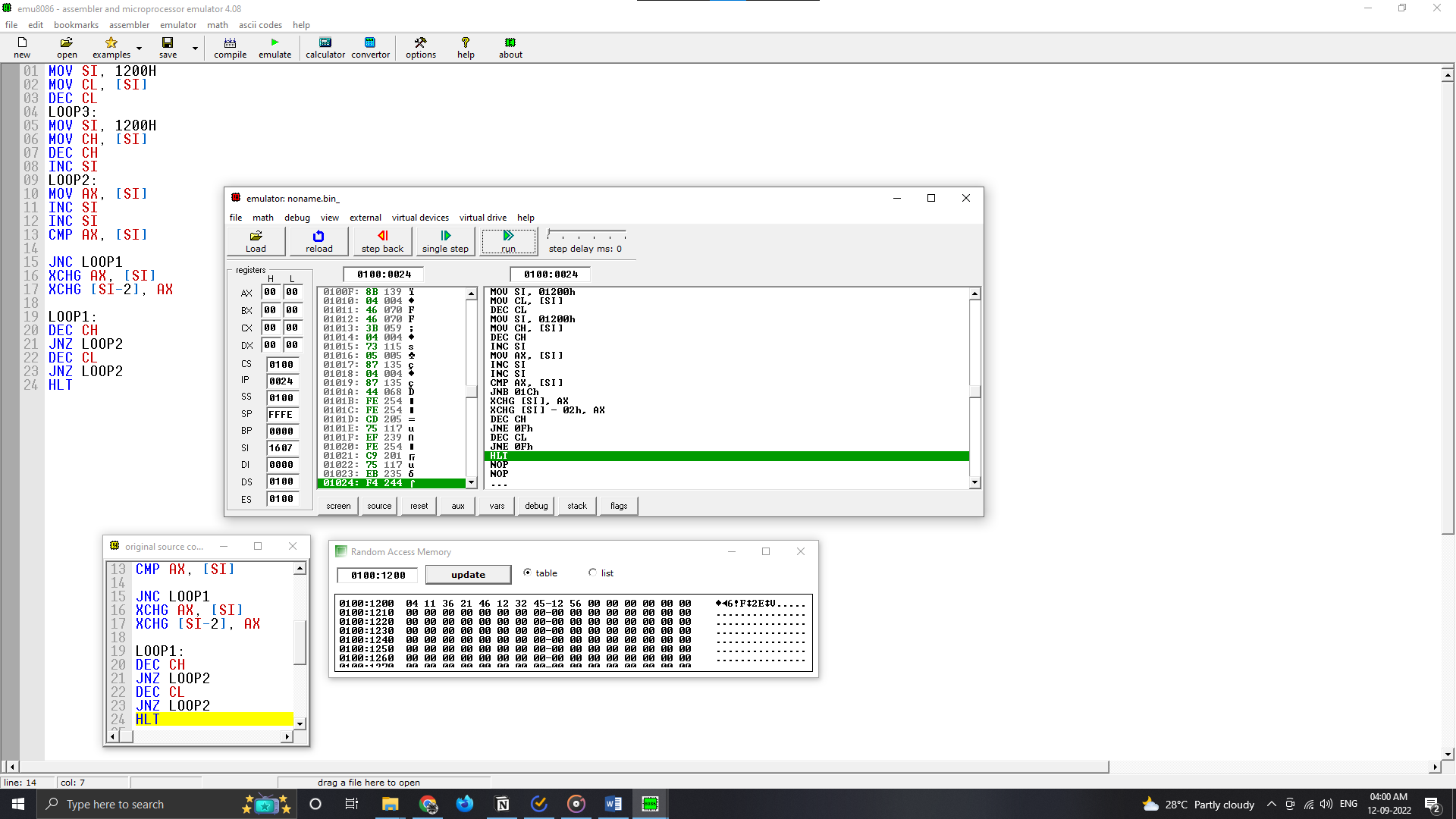
**Microprocessor, Microcontroller, and Interface Techniques Lab**

**Experiment 3 Output**

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